

09/826,383

04/04/2001

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Filing Date

**Application Number** 

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FORM			First Named Inventor	Sh	Shigeyoshi Yoshida					
(to be used for all correspondence after initial filing)			Group Art Unit		2815					
			Examiner Name		NA					
Total Number of Pages in This Submission 5			Attorney Docket Numbe	r	0694-143					
ENCLOSURES (check all that apply)										
Fee Transmittal Form Fee Attached  Amendment / Response After Final Affidavits/declaration(s)  Extension of Time Request  Express Abandonment Request		Drawing  Licensir  Petition and Acc  To Conv Provisio  Power of Change Address Termina  Small E	Assignment Papers (for an Application)  Drawing(s)  Licensing-related Papers  Petition Routing Slip (PTO/SB/69) and Accompanying Petition  To Convert a Provisional Application  Power of Attorney, Revocation Change of Correspondence Address Terminal Disclaimer  Small Entity Statement		After Allowance Communication to Group Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Proprietary Information  Status Letter Additional Enclosure(s) (please identify below):  copies of ref. cited in IDS statement, return receipt postcard					
Information Disclosure Statement  Certified Copy of Priority			et for Refund	·	<u>.</u>					
Document(s)		Remarks			<u> </u>					
Response to Missing Parts/ Incomplete Application  Response to Missing Parts under 37 CFR 1.52 or 1.53			_		ECHAOLOGY CE	့ တ	VI.CLI.			
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT										
Firm <i>or</i> Individual name	Bradley N. Rube	N'					<b>'-</b>			
Signature	Signature Badfroll									
Date	12 October 2001									

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 12 October 2001

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## IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Serial No.:	Filed:	Applicant:	Atty Dkt:
09/826,383	4 April 2001	Yoshida, et al	0694-143

Title: ELECTRONIC NOISE SUPPRESSOR, SEMICONDUCTOR DEVICE USING THE SAME, AND METHOD OF MANUFACTURING THE SAME.

Examiner: NA Art Unit: 2815

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231-0001

## **Information Disclosure Statement**

Dear Sir:

In connection with the above-identified application, enclosed is a search report and copies of references cited by the European Patent Office. This information is submitted under C.F.R. § 1.97(b)(3) with all references listed on the corresponding 1449/PTO form and copies of the cited references are enclosed.

Respectfully Submitted

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12 October 2001